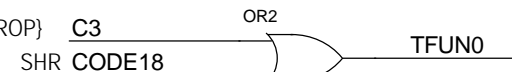
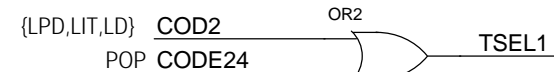
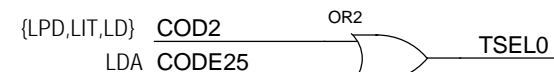
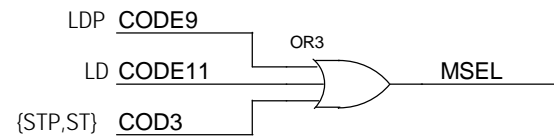
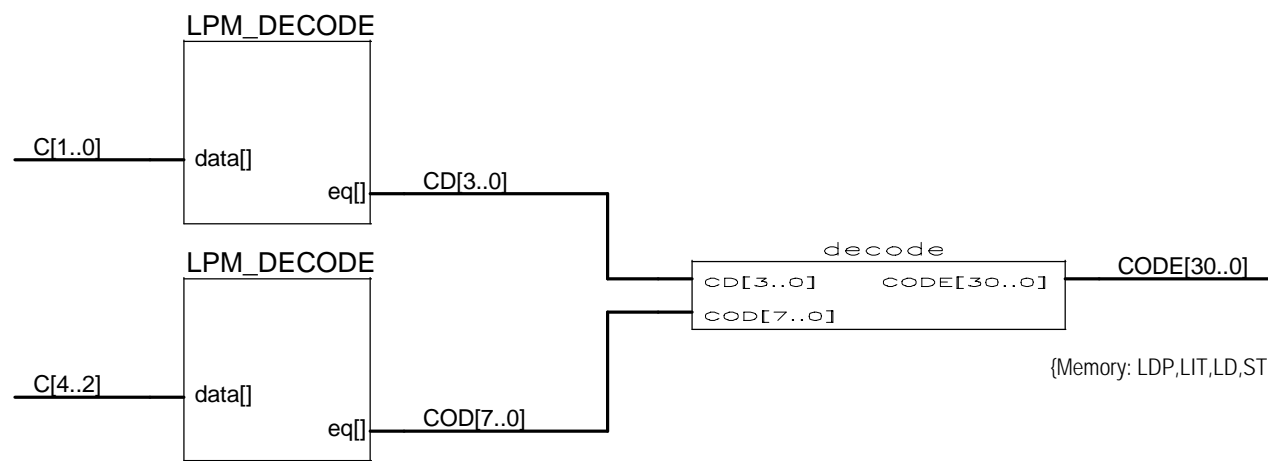


C[4..0] INPUT



OUTPUT ACE

OUTPUT ALU0

OUTPUT ALU1

OUTPUT TFUN0

OUTPUT TFUN1

OUTPUT CARRY

OUTPUT MSEL

OUTPUT NCE

OUTPUT NPUSH

OUTPUT ODD

OUTPUT PCE

OUTPUT PLOAD

OUTPUT PSEL

OUTPUT RCE

OUTPUT RPUSH

OUTPUT RSEL

OUTPUT /RD

OUTPUT TCE

OUTPUT TSEL0

OUTPUT TSEL1

OUTPUT /WR

OUTPUT ZERO

OUTPUT ALOAD

OUTPUT D_OE

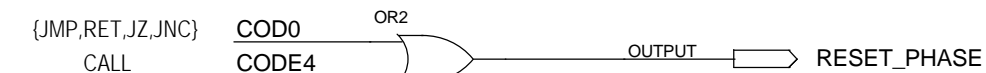
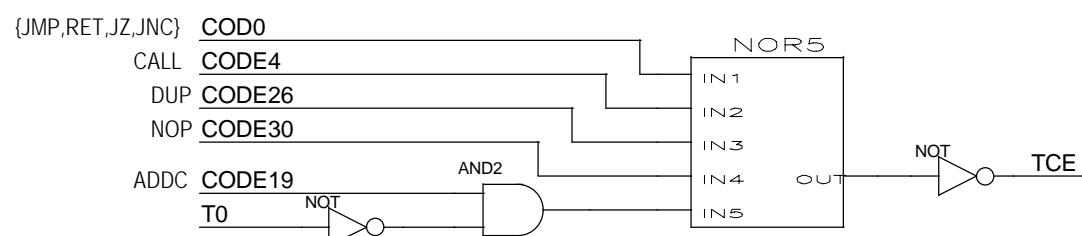
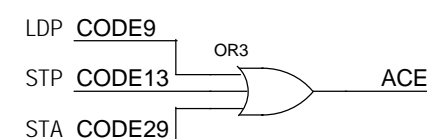
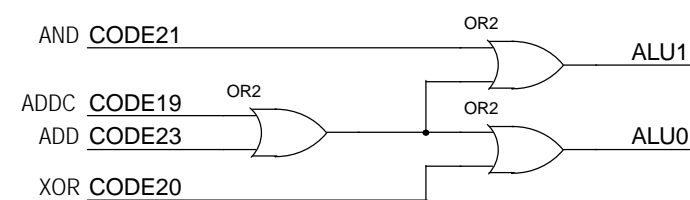
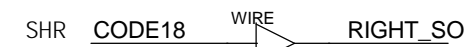
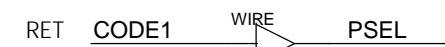
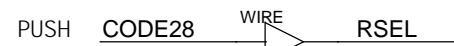
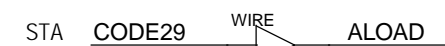
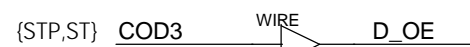
OUTPUT RESET_PHASE

OUTPUT RIGHT_SO

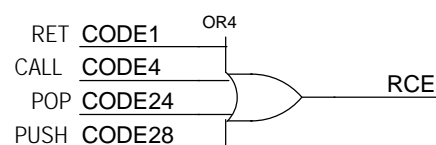
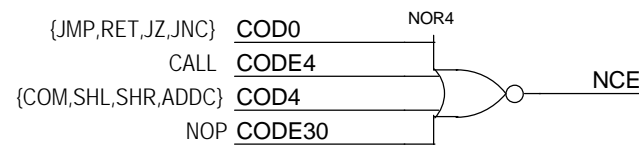
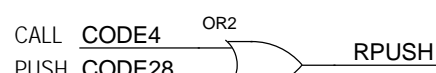
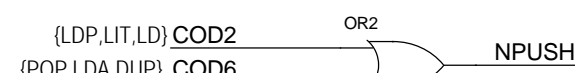
T[15..0] INPUT



T16 INPUT



OUTPUT RIGHT_SO

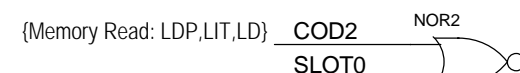
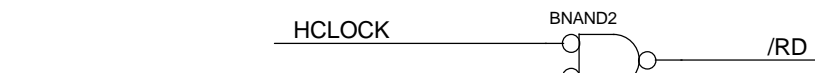
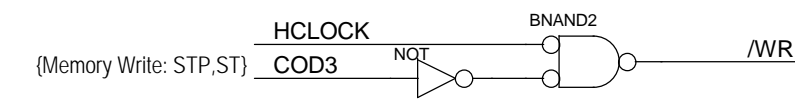
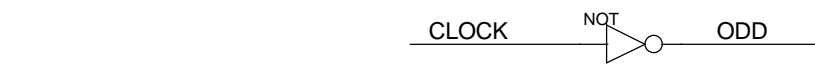
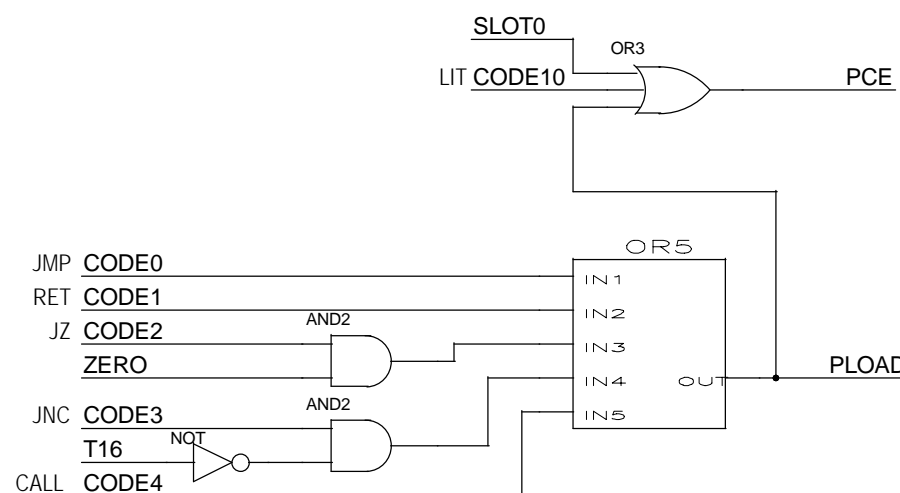


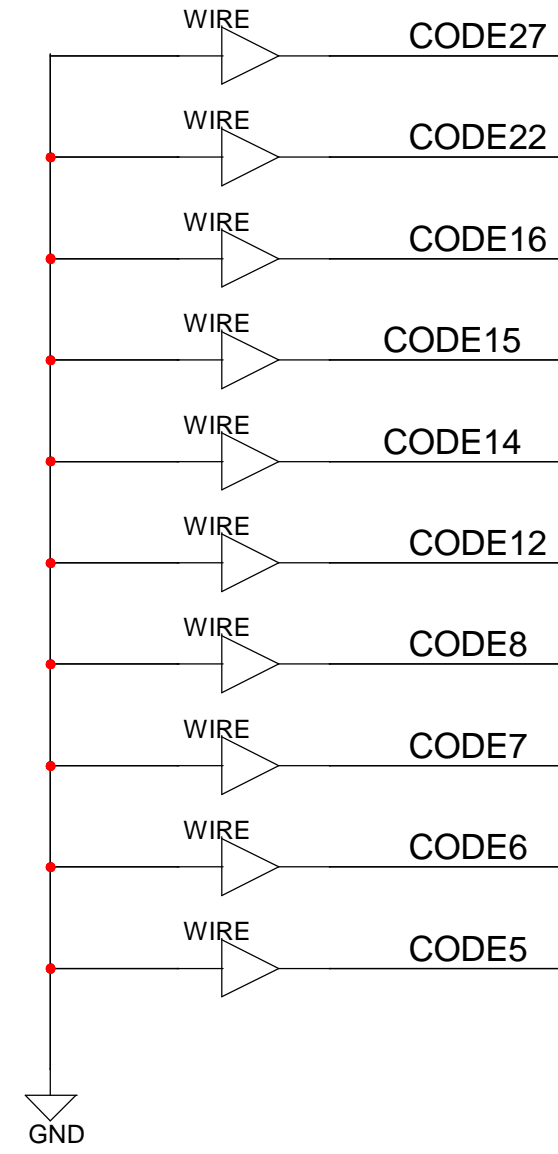
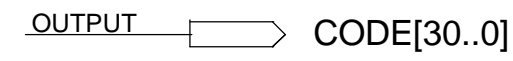
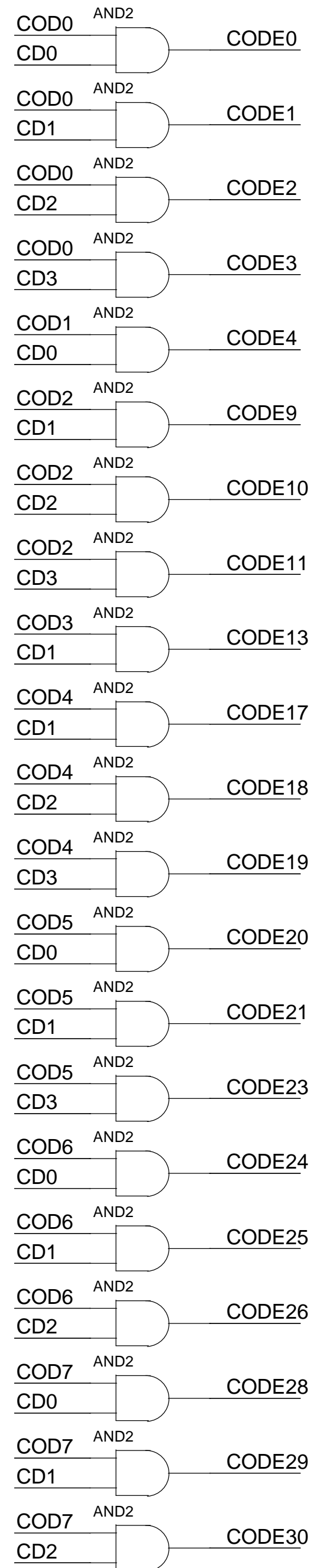
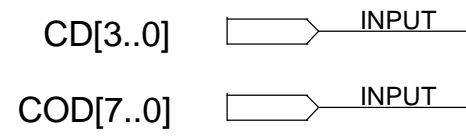
(NCE=LDP,LIT,LD,STP,ST,XOR,AND,ADD,POP,LDA,DUP,PUSH,STA,DROP)
 (NCE=(C3 & !CODE30) + COD5)

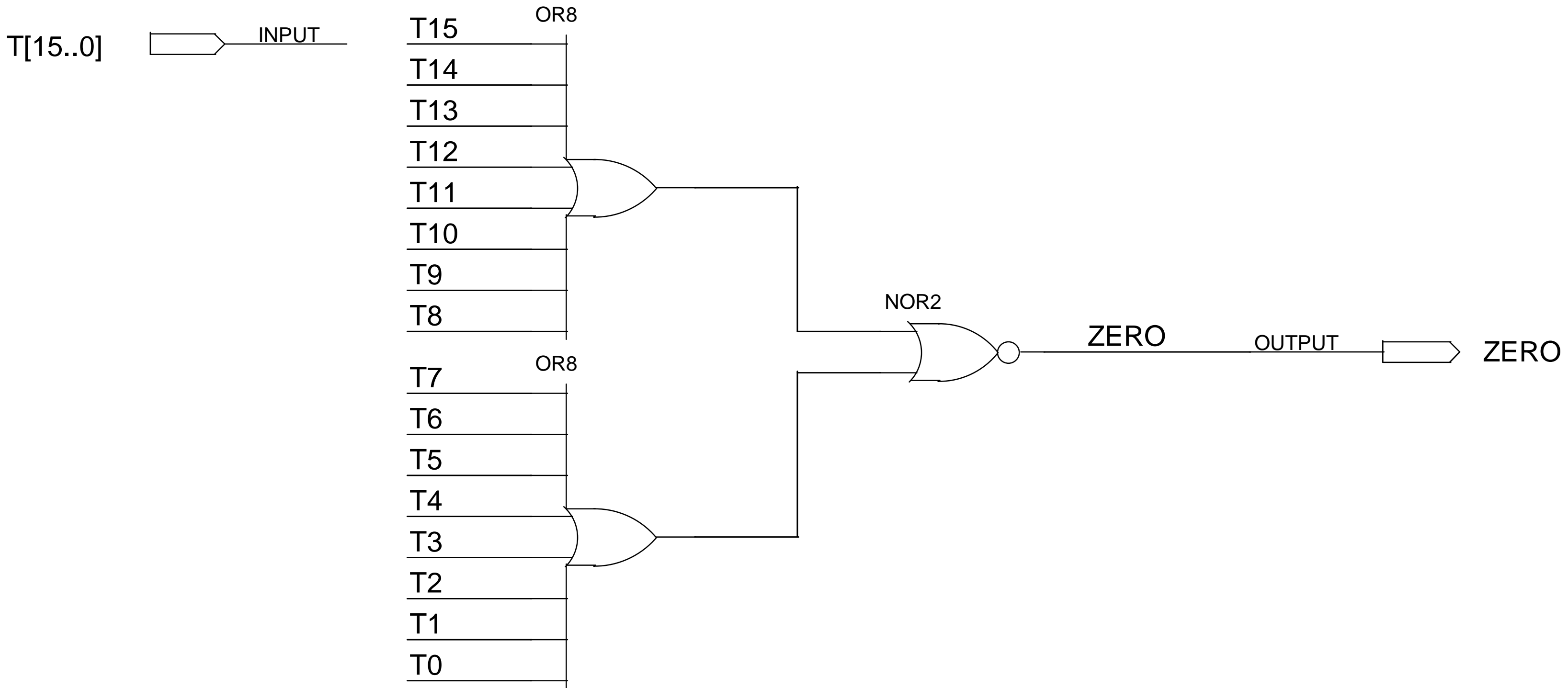
HCLOCK INPUT

CLOCK INPUT

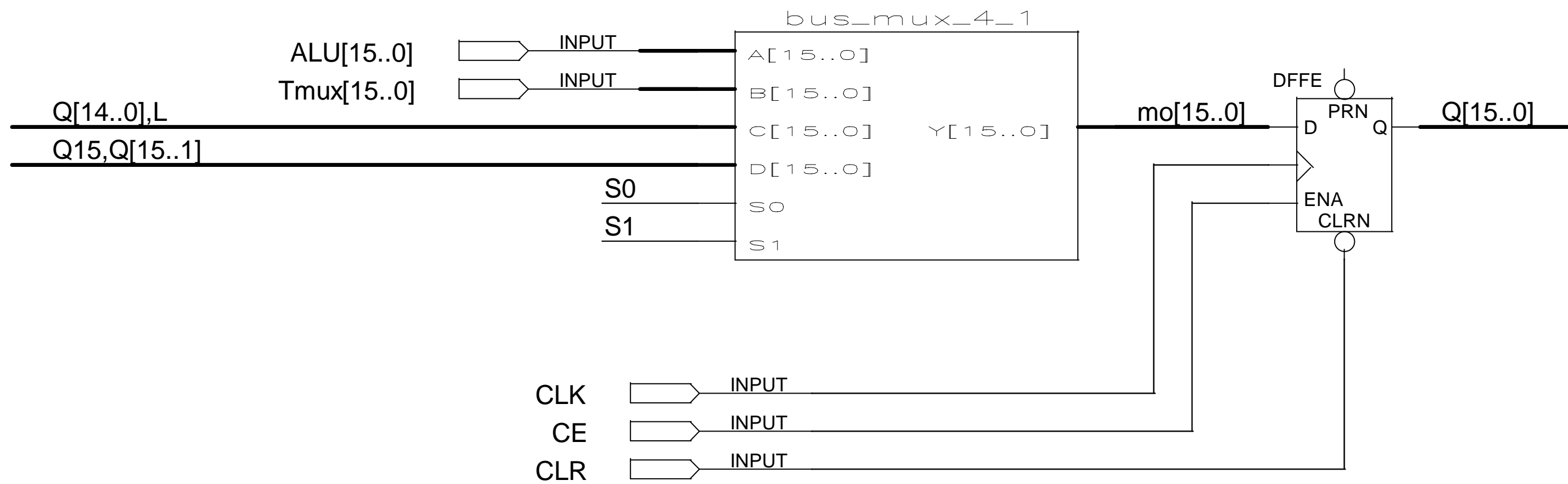
SLOT0 INPUT



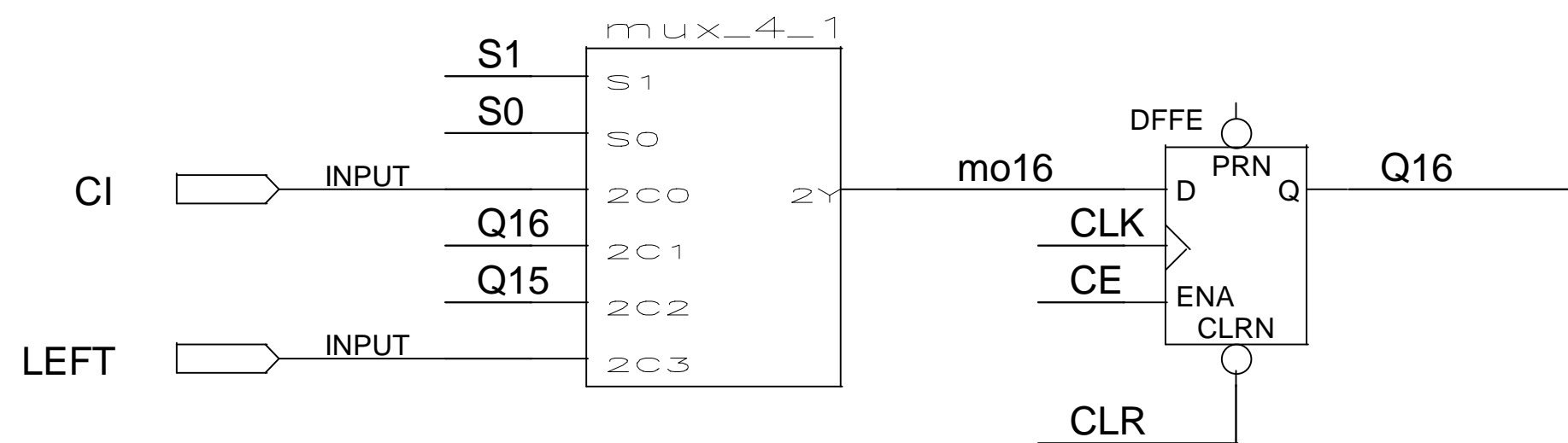
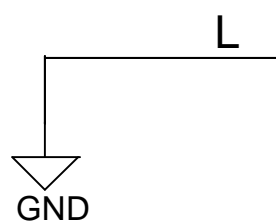


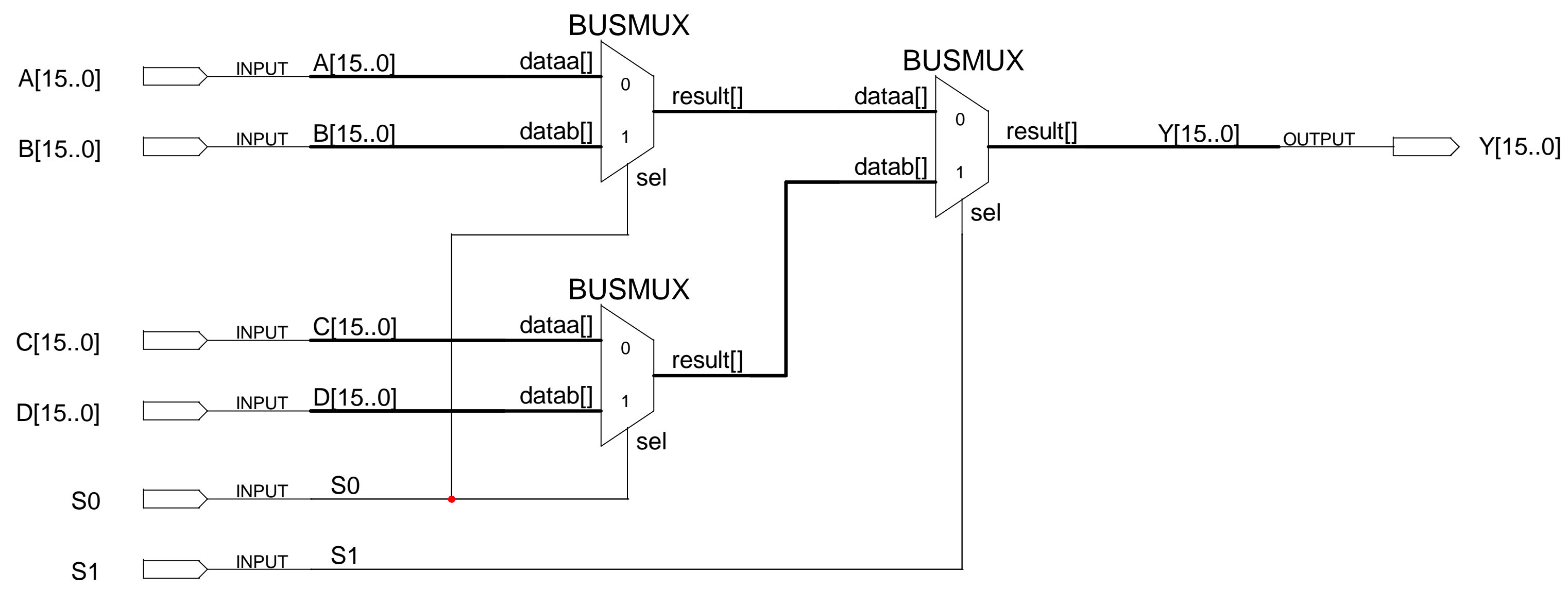


S0 INPUT
S1 INPUT



OUTPUT Q[15..0]
OUTPUT Q16

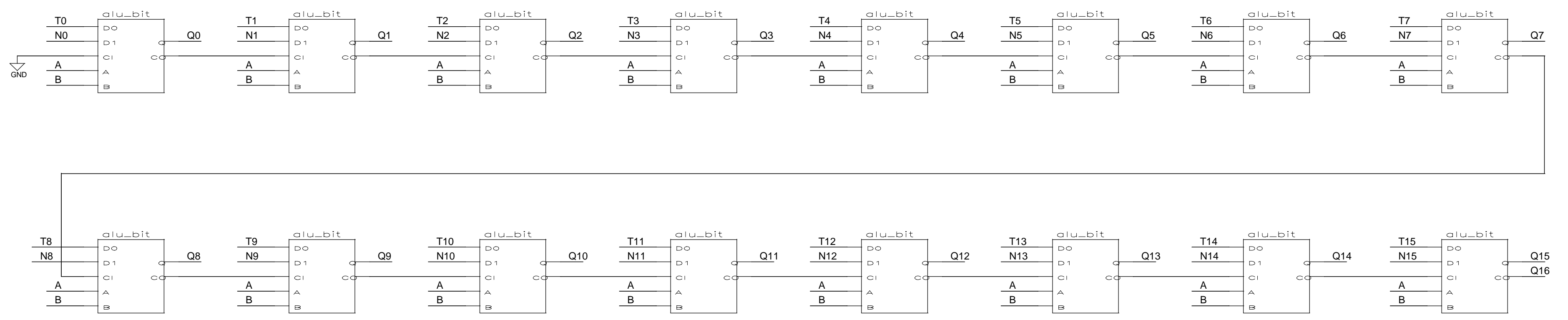


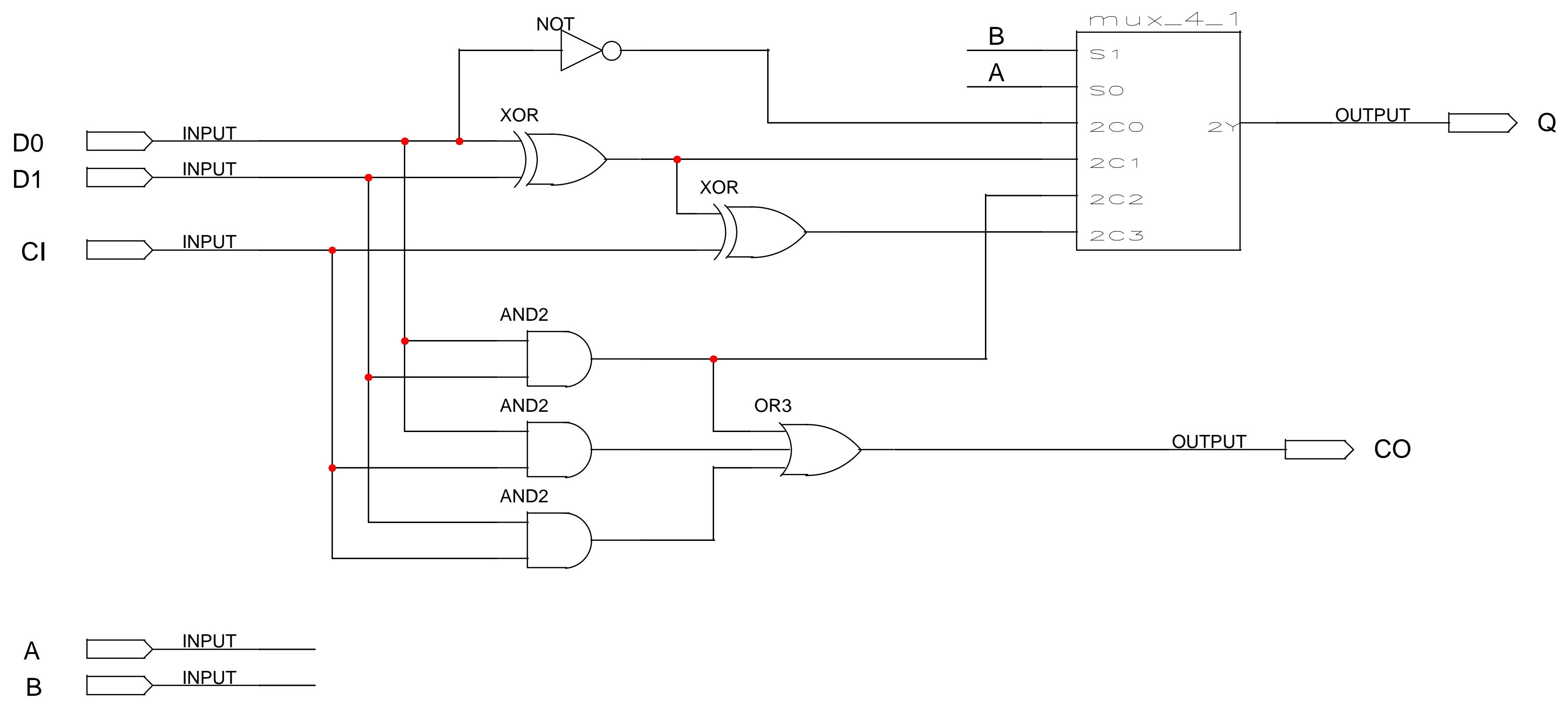


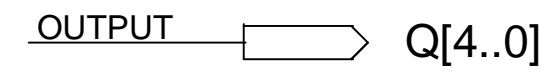
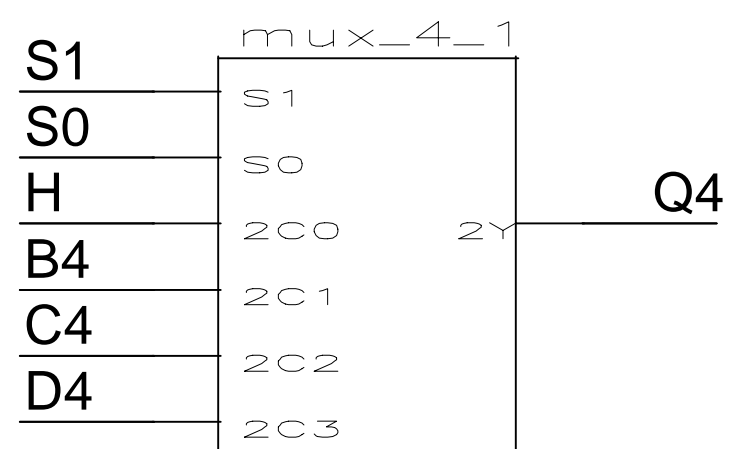
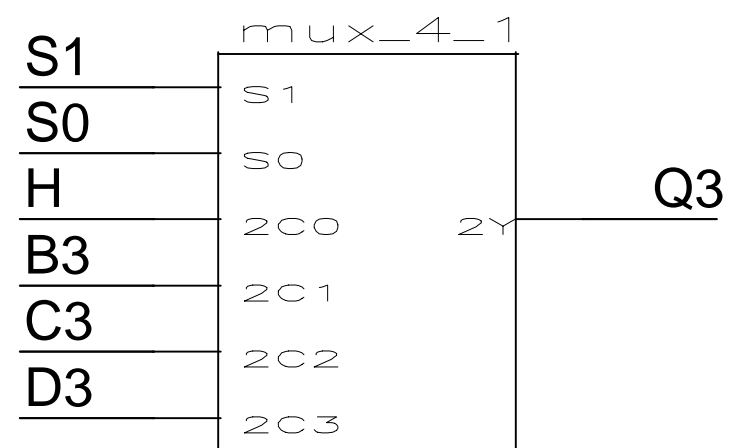
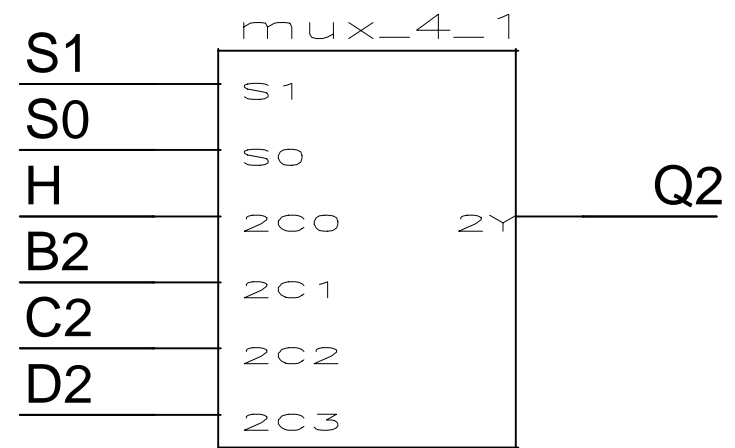
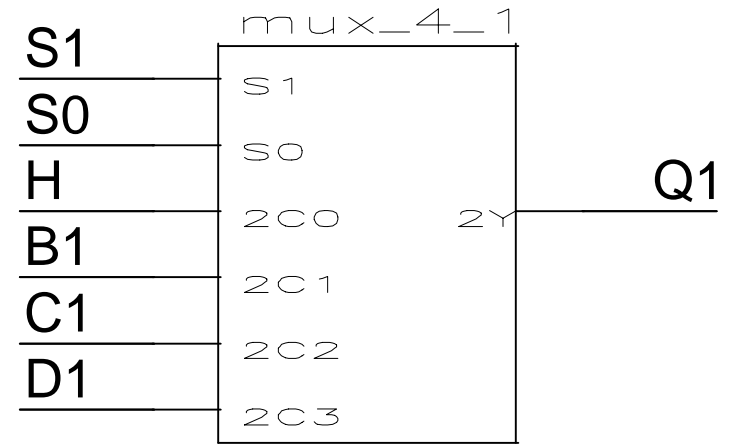
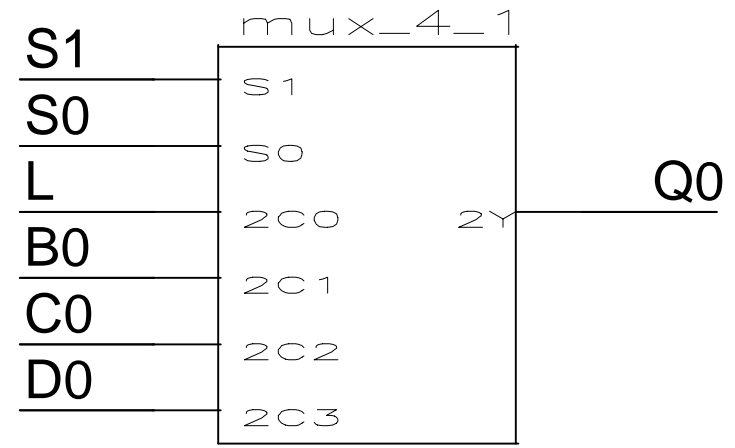
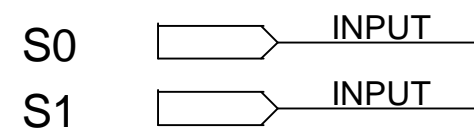
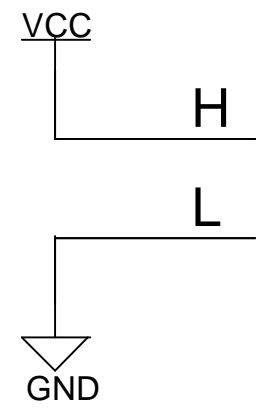
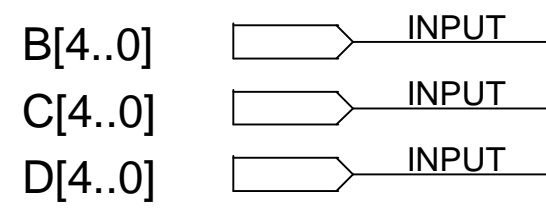
S1	S0	Y
0	0	A
0	1	B
1	0	C
1	1	D

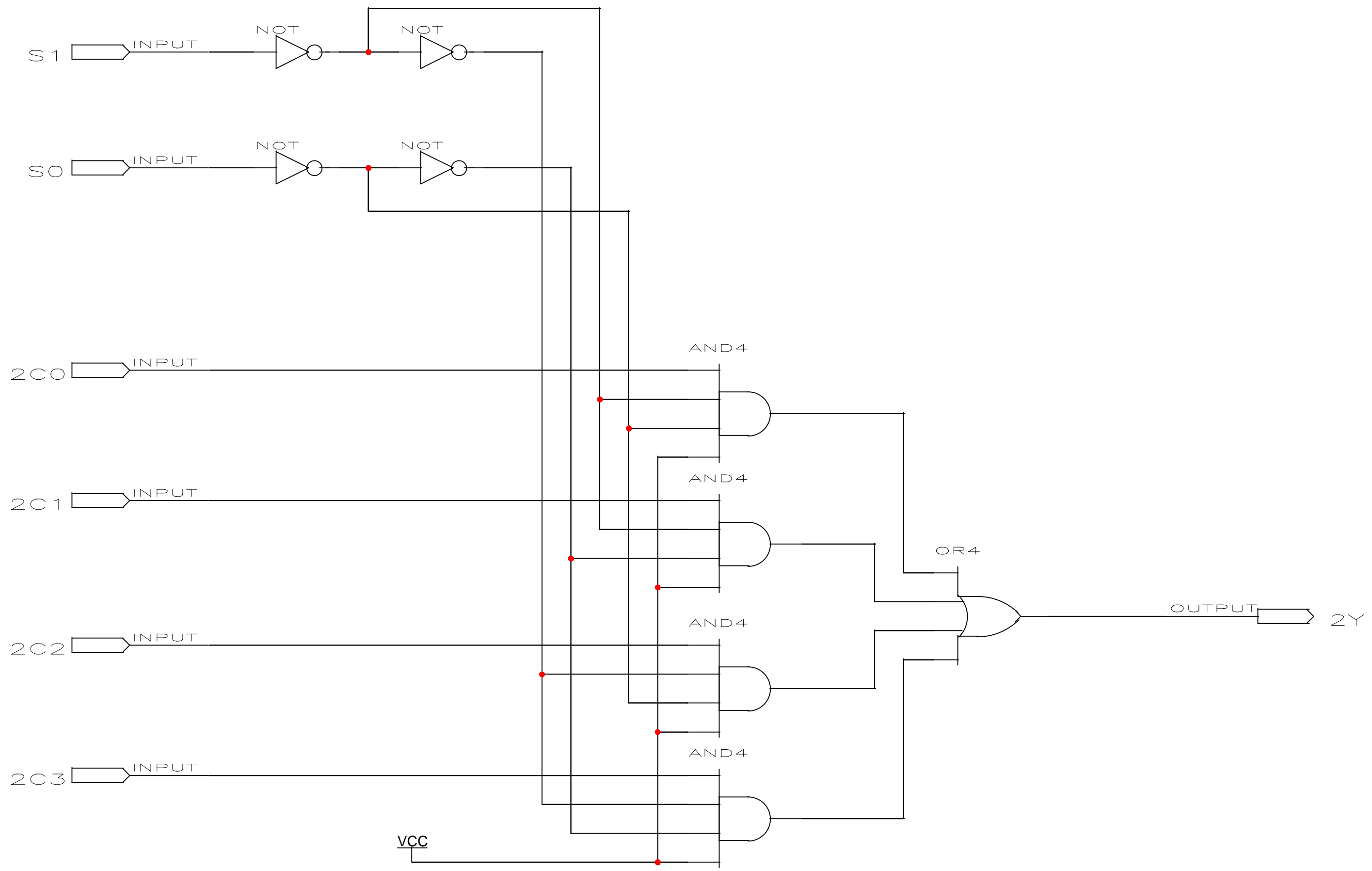
T[15..0] INPUT
N[15..0] INPUT
A INPUT
B INPUT

OUTPUT Q[15..0]
OUTPUT Q16

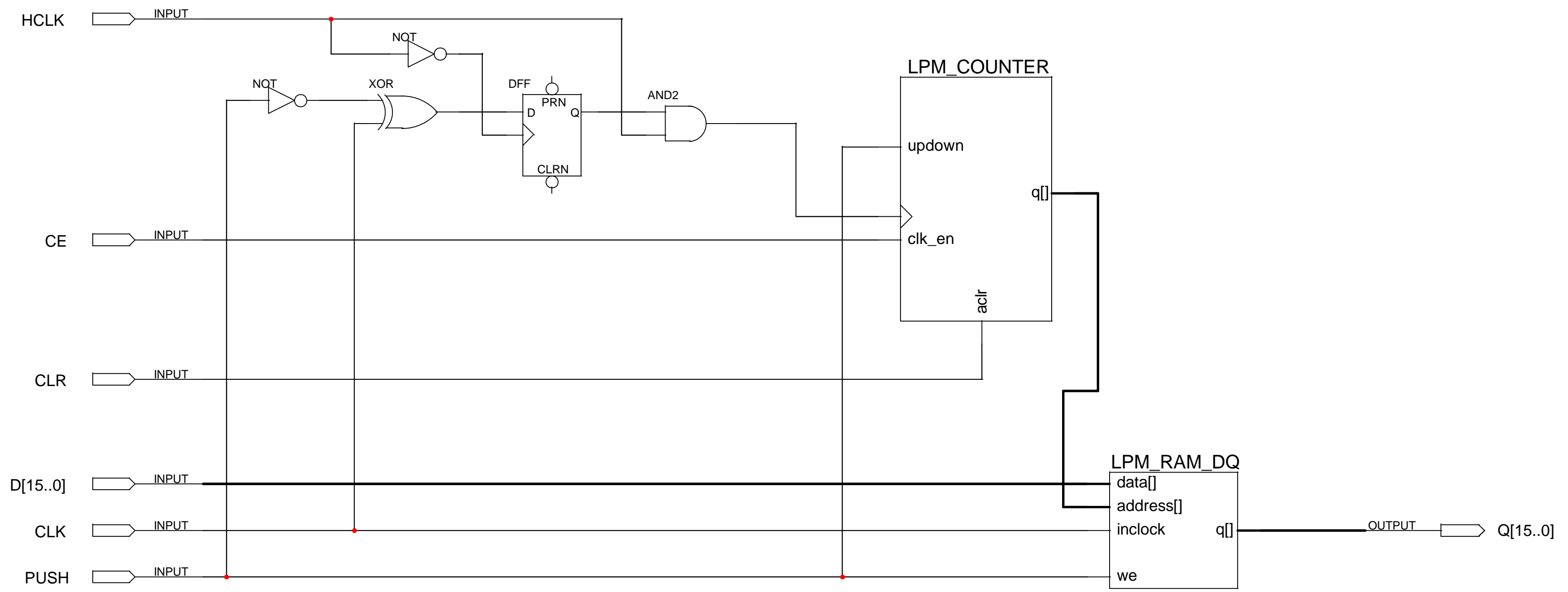




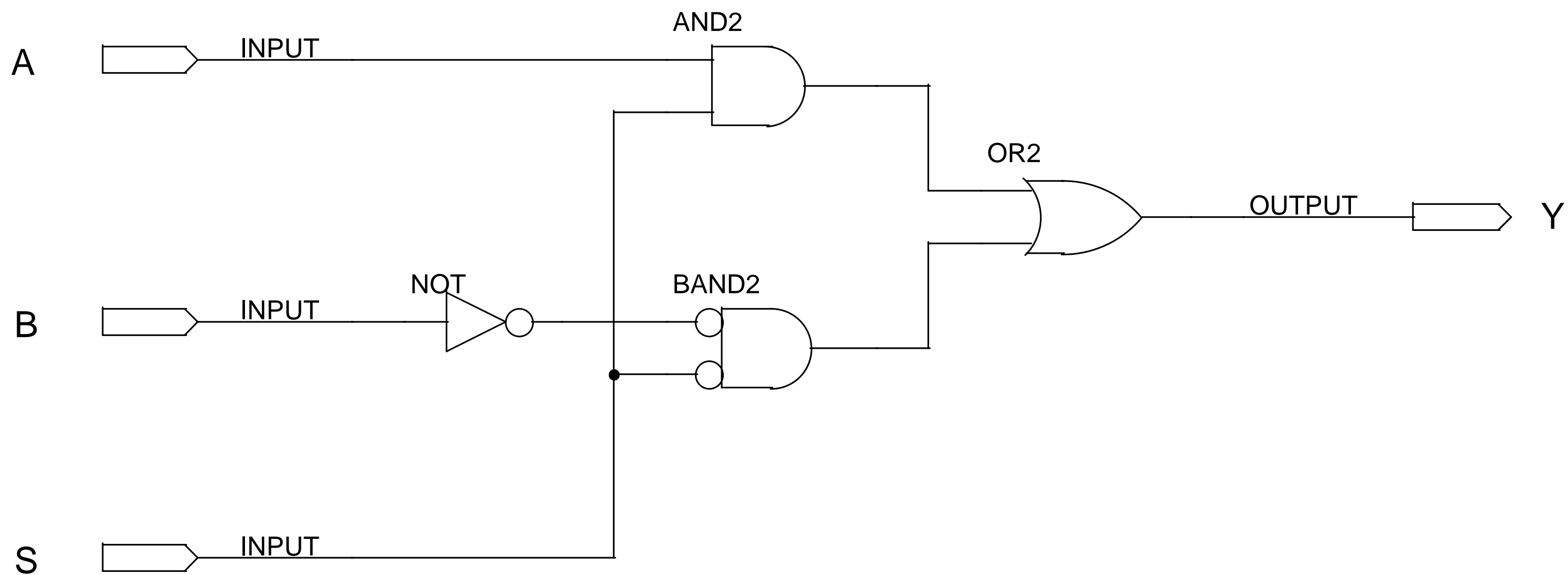




S1	S0	Y
0	0	C0
0	1	C1
1	0	C2
1	1	C3



21MUX



TITLE				MacroFunction			
COMPANY				ALTERA CORPORATION			
DESIGNER				Applications Engineering			
SIZE	A	EPLD	AUTO	NUMBER	1.00	REV	A
DATE	12:21p 5-17-1999			SHEET	1	OF	1
TURBO	ON			SECURITY	OFF		